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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HD74HC123A

Dual Retriggerable Monostable Multivibrators (with Clear)

REJ03D0564-0200
(Previous ADE-205-438)
Rev.2.00
Oct 11, 2005

Description

This multivibrator features both a negative, A, and a positive, B, transition triggered input, either of which can be used as an inhibit input. Also included is a clear input that when taken low resets the one shot. The HD74HC123A can be triggered on the positive transition of the clear while A is held low and B is held high.

The HD74HC123A is retriggerable. That is it may be triggered repeatedly while their outputs are generating a pulse and the pulse will be extended.

Pulse width stability over a wide range of temperature. The output pulse equation is simply: $t_w = (R_{ext}) (C_{ext})$.

Features

- High Speed Operation
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2$ to 6 V
- Low Input Current: 1 μA max
- Low Quiescent Supply Current
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC123AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	P	—
HD74HC123AFPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

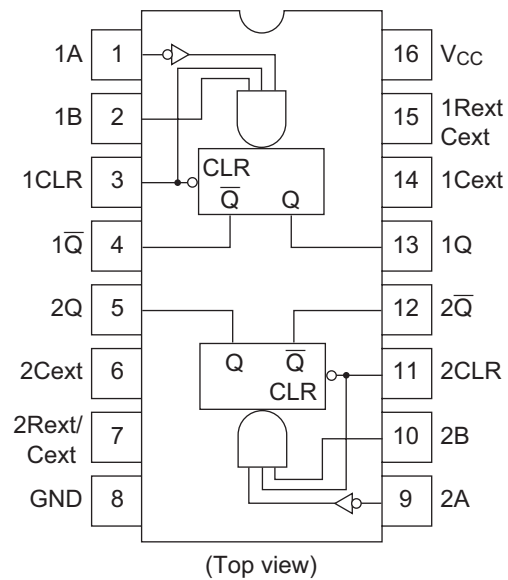
Function Table

Inputs			Outputs	
Clear	A	B	Q	$\bar{Q}$
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	L			
H		H		
	L	H		

Note: External timing capacitance connects between Cext and Rext/Cext.

H : High level
L : Low level
X : Irrelevant

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V_{CC}	-0.5 to 7.0	V
Input / Output voltage	V_{in}, V_{out}	-0.5 to $V_{CC} + 0.5$	V
Input / Output diode current	I_{IK}, I_{OK}	± 20	mA
Output current	I_O	± 25	mA
V_{CC} , GND current	I_{CC} or I_{GND}	± 50	mA
Power dissipation	P_T	500	mW
Storage temperature	T_{stg}	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	2 to 6	V	
Input / Output voltage	V_{IN}, V_{OUT}	0 to V_{CC}	V	
Operating temperature	T_a	-40 to 85	°C	
Input rise / fall time ^{*1}	t_r, t_f	0 to 1000	ns	$V_{CC} = 2.0 \text{ V}$
		0 to 500		$V_{CC} = 4.5 \text{ V}$
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

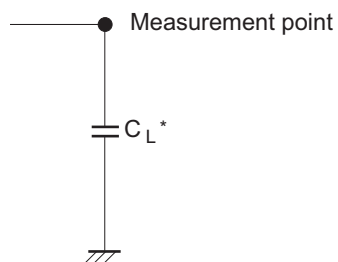
Electrical Characteristics

Item	Symbol	V _{CC} (V)	Ta = 25°C			Ta = -40 to+85°C		Unit	Test Conditions			
			Min	Typ	Max	Min	Max					
Input voltage	V _{IH}	2.0	1.5	—	—	1.5	—	V				
		4.5	3.15	—	—	3.15	—					
		6.0	4.2	—	—	4.2	—					
	V _{IL}	2.0	—	—	0.5	—	0.5	V				
		4.5	—	—	1.35	—	1.35					
		6.0	—	—	1.8	—	1.8					
Output voltage	V _{OH}	2.0	1.9	2.0	—	1.9	—	V	Vin = V _{IH} or V _{IL}	I _{OH} = -20 μA		
		4.5	4.4	4.5	—	4.4	—					
		6.0	5.9	6.0	—	5.9	—					
		4.5	4.18	—	—	4.13	—			I _{OH} = -4 mA		
		6.0	5.68	—	—	5.63	—			I _{OH} = -5.2 mA		
	V _{OL}	2.0	—	0.0	0.1	—	0.1	V	Vin = V _{IH} or V _{IL}	I _{OL} = 20 μA		
		4.5	—	0.0	0.1	—	0.1					
		6.0	—	0.0	0.1	—	0.1					
		4.5	—	—	0.26	—	0.33			I _{OL} = 4 mA		
		6.0	—	—	0.26	—	0.33			I _{OL} = 5.2 mA		
	Input current		I _{in}	6.0	—	—	±0.1	—	±1.0	μA	Vin = V _{CC} or GND	
	Quiescent supply current	Standby state	I _{CC}	6.0	—	—	130	—	220	μA	Vin = V _{CC} or GND	I _{out} = 0 μA
Active state		6.0		—	—	130	—	220	Rext/Cext = 0.5 V _{CC}			

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

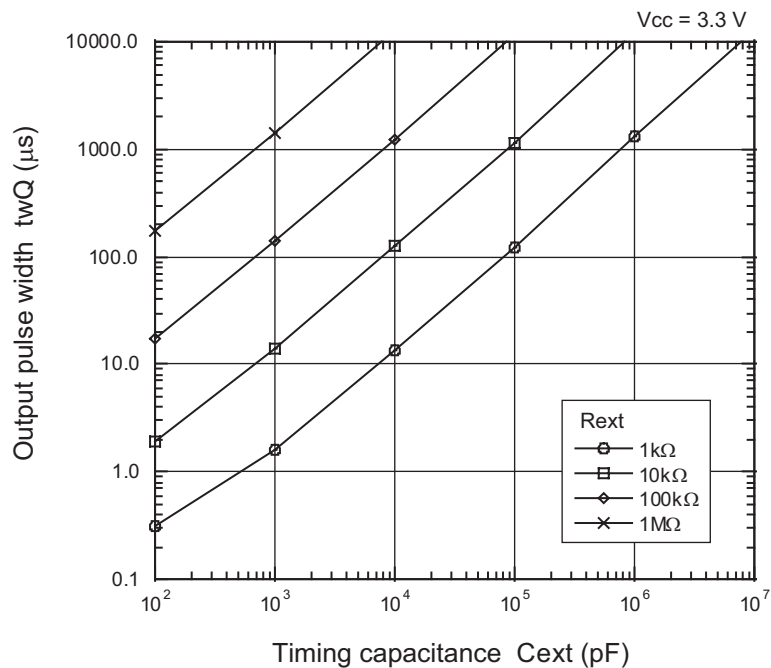
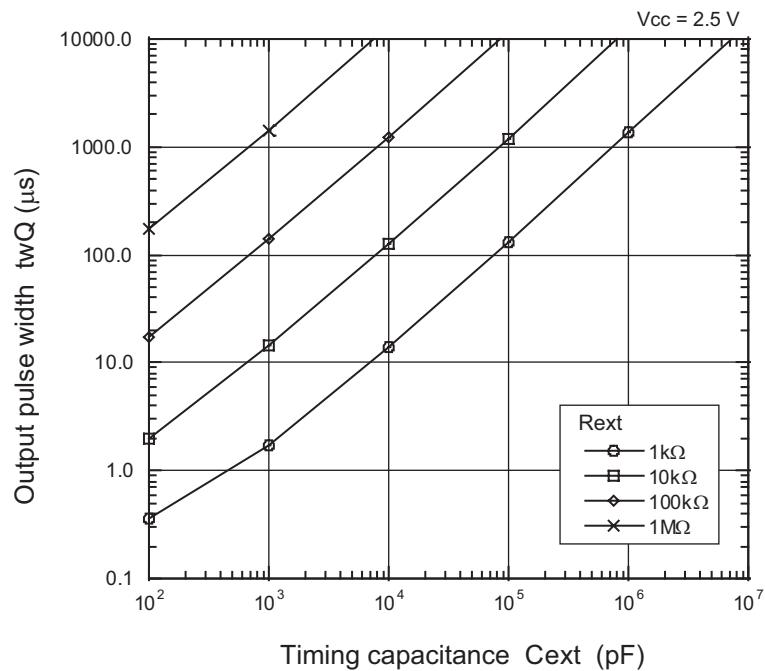
Item	Symbol	$V_{CC} \text{ (V)}$	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } +85^\circ\text{C}$		Unit	Test Conditions	
			Min	Typ	Max	Min	Max			
Propagation delay time	t_{PLH}	2.0	—	—	210	—	265	ns	A, B or Clear to Q	
		4.5	—	22	42	—	53			
		6.0	—	—	36	—	45			
	t_{PHL}	2.0	—	—	240	—	300	ns	A, B or Clear to $\bar{Q}$	
		4.5	—	23	48	—	60			
		6.0	—	—	41	—	51			
	t_{PHL}	2.0	—	—	170	—	215	ns	Clear to Q	
		4.5	—	18	34	—	43			
		6.0	—	—	29	—	37			
	t_{PLH}	2.0	—	—	180	—	225	ns	Clear to $\bar{Q}$	
		4.5	—	16	36	—	45			
		6.0	—	—	31	—	38			
Output rise/fall time	t_{TLH}, t_{THL}	2.0	—	—	75	—	95	ns		
		4.5	—	5	15	—	19			
		6.0	—	—	13	—	16			
Pulse width	t_w	2.0	150	—	—	190	—	ns	A, B, Clear	
		4.5	30	6	—	38	—			
		6.0	26	—	—	33	—			
Minimum output pulse width	$t_{wQ(\min)}$	2.0	—	1.5	—	—	—	μs	Cext = 28 pF	Rext = 6 k Ω
		4.5	—	450	—	—	—	ns		Rext = 2 k Ω
		6.0	—	380	—	—	—			
Output pulse width	t_{wQ}	4.5	—	1.0	—	—	—	ms	Cext = 0.1 μF , Rext = 10 k Ω	
Input capacitance	Cin	—	—	5	10	—	10	pF		

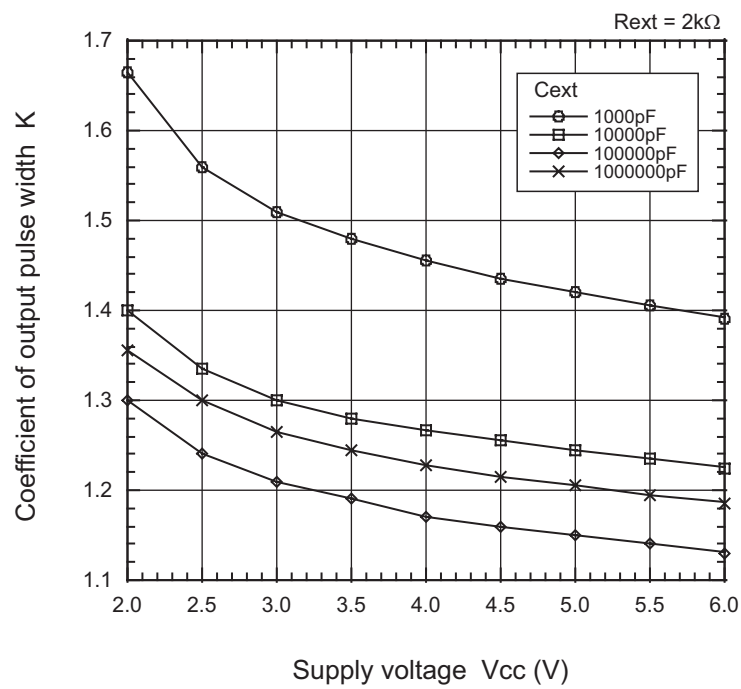
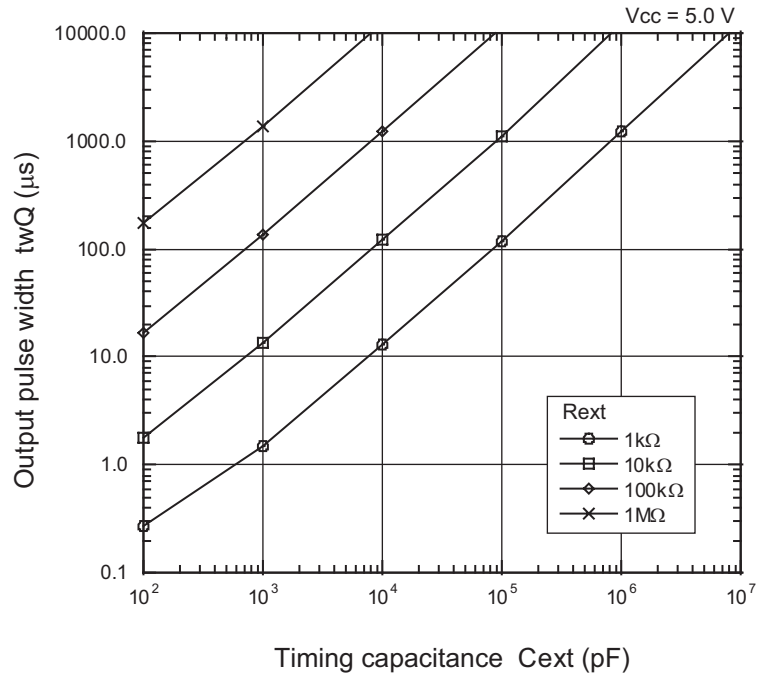
Caution in use: In order to prevent any malfunctions due to noise, connect a high-frequency performance capacitor between V_{CC} and GND, and keep the wiring between the External components and Cext, Rext/Cext pins as short as possible.

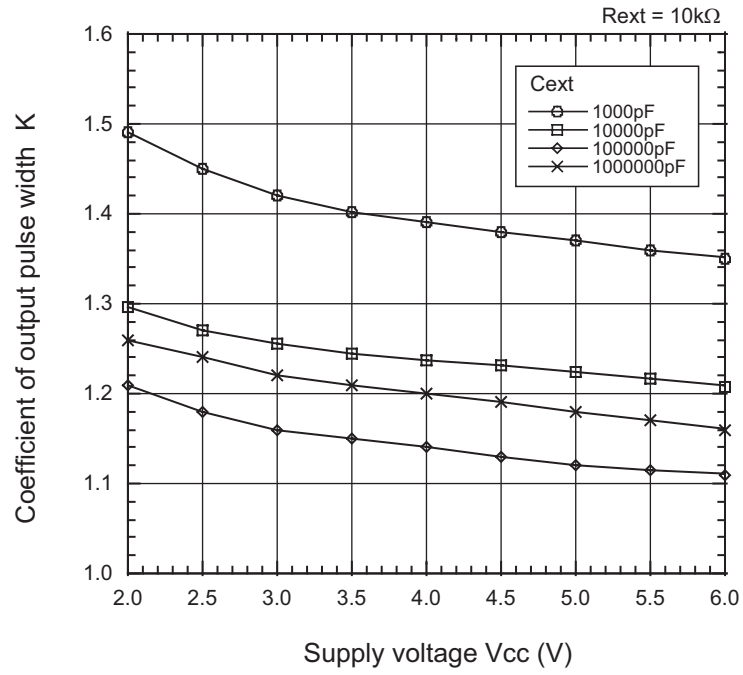
Test Circuit

Note: C_L includes the probe and jig capacitance.

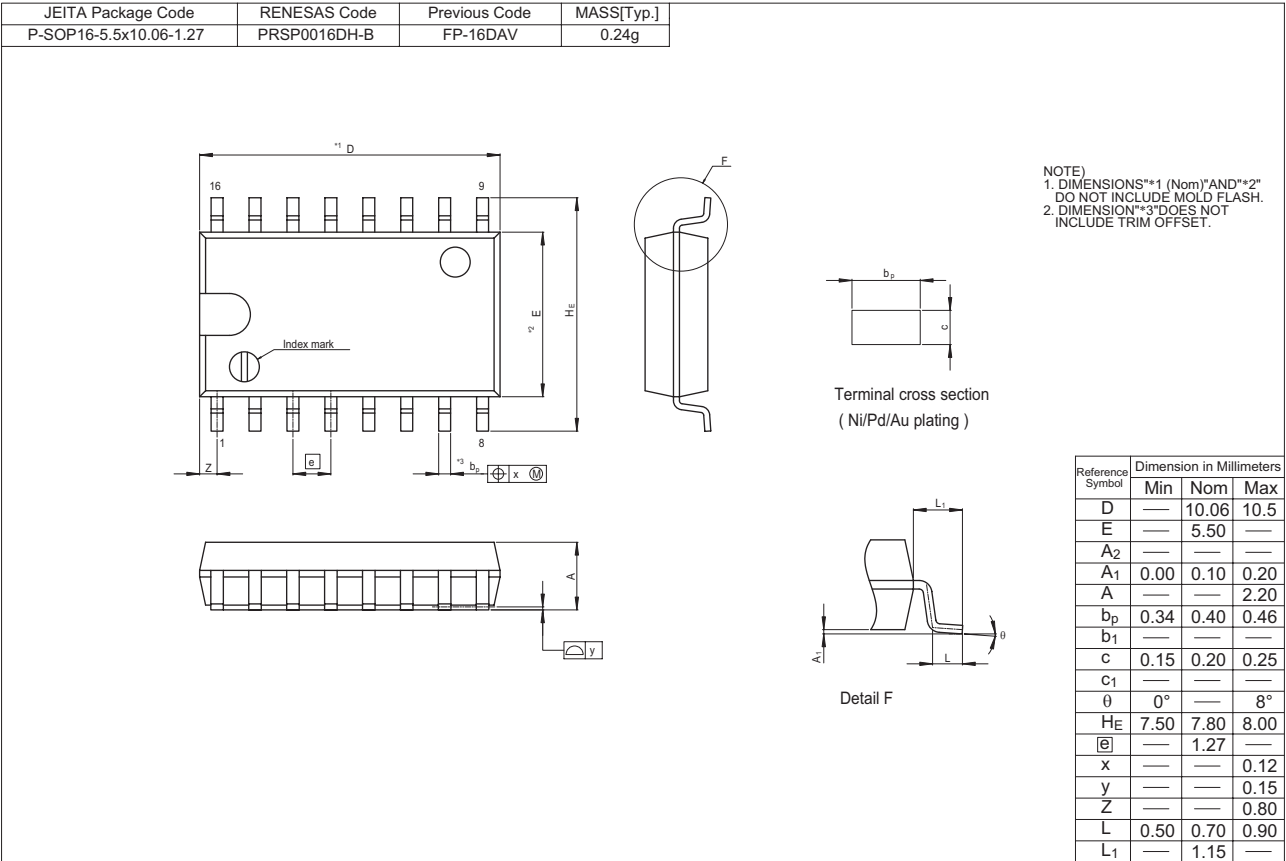
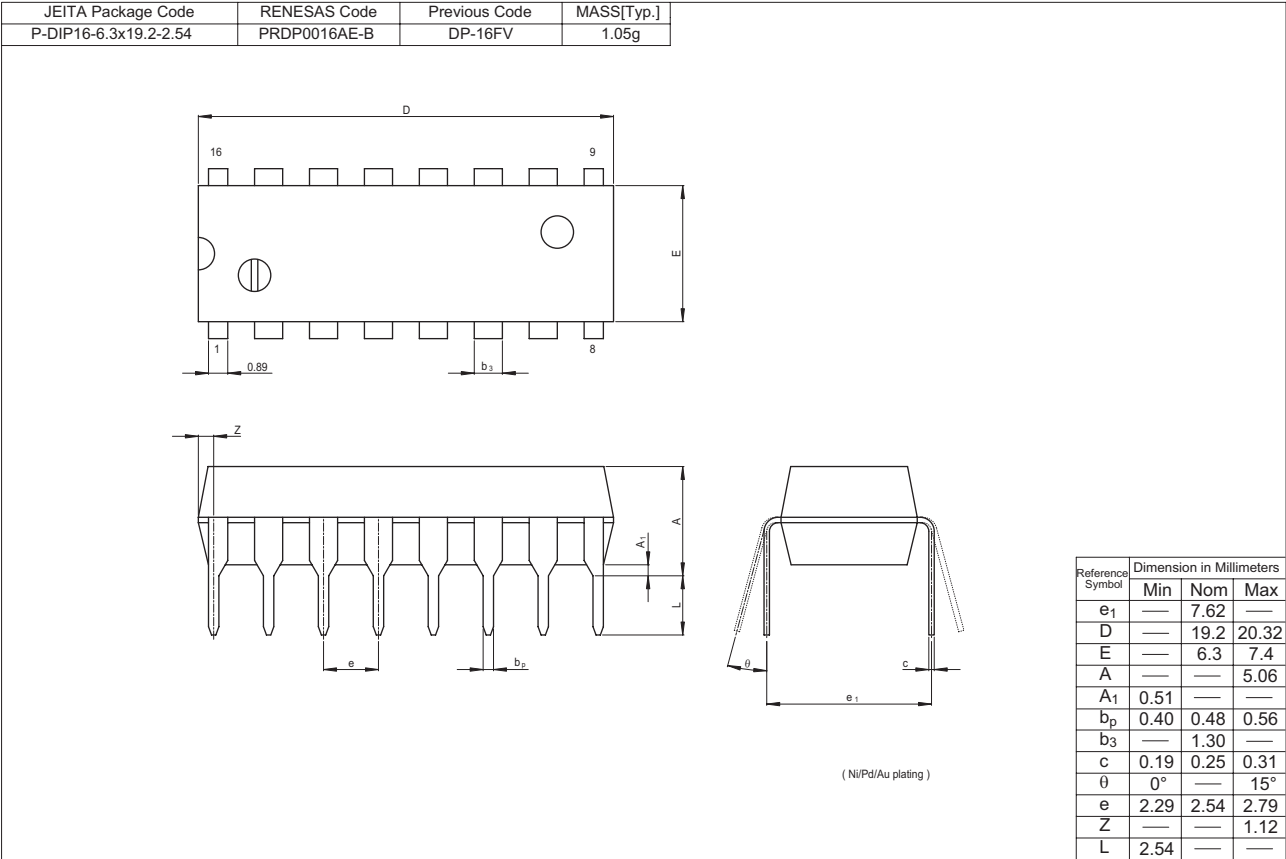
Application Data







Package Dimensions



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